SKHI 22 A / B (R) ...



Hybrid Dual IGBT Driver

SKHI 22 A / B (R)

Preliminary Data

Features

- Double driver for halfbridge IGBT modules
- SKHI 22A is compatible to old SKHI 22
- SKHI 22B has additional functionality
- · CMOS compatible inputs
- Short circuit potection by V_{CE} monitoring and switch off
- Drive interlock top / bottom
- Isolation by transformers
- Supply undervotage protection (13 V)
- Error latch / output

Typical Applications

- Driver for IGBT modules in bridge circuits in choppers, inverter drives, UPS and welding inverters
- 1) see fig. 6
- ²⁾ At R_{CE} = 18 k Ω , C_{CE} = 330 pF

| Absolute Maximum Ratings | | | | | | | |
|--------------------------|--|----------------------|-------|--|--|--|--|
| Symbol | Conditions | Values | Units | | | | |
| V _S | Supply voltage prim. | 18 | V | | | | |
| V _{iH} | Input signal volt. (High) SKHI 22A | V _S + 0,3 | V | | | | |
| | SKHI 22B | 5 + 0,3 | V | | | | |
| Iout _{PEAK} | Output peak current | 8 | Α | | | | |
| Iout _{AVmax} | Output average current | 40 | mA | | | | |
| f _{max} | max. switching frequency | 50 | kHz | | | | |
| V _{CE} | Collector emitter voltage sense across the IGBT | 1200 | V | | | | |
| dv/dt | Rate of rise and fall of voltage secondary to primary side | 50 | kV/μs | | | | |
| $V_{\rm isollO}$ | Isolation test voltage | 2500 | Vac | | | | |
| | input - output (2 sec. AC) | | | | | | |
| V _{isol12} | Isolation test voltage | 1500 | V | | | | |
| | output 1 - output 2 (2 sec. AC) | | | | | | |
| R_{Gonmin} | Minimum rating for R _{Gon} | 3 | Ω | | | | |
| R_{Goffmin} | Minimum rating for R _{Goff} | 3 | Ω | | | | |
| Q _{out/pulse} | Max. rating for output charge per pulse | 4 ¹⁾ | μC | | | | |
| T _{op} | Operating temperature | - 40 + 85 | °C | | | | |
| T _{stg} | Storage temperature | - 40 + 85 | °C | | | | |

| Characte | Characteristics $T_a = 25 ^{\circ}\text{C}$, unless otherwise specified | | | | | | |
|------------------------|---|--------------|-----------------|------|-------------------|--|--|
| Symbol | Conditions | min. | typ. | max. | Units | | |
| V_S | Supply voltage primary side | 14,4 | 15 | 15,6 | V | | |
| I _{so} | Supply current primary side (no load) | | 80 | | mA | | |
| | Supply current primary side (max.) | | | 290 | mA | | |
| V_{i} | Input signal voltage SKHI 22A on/off | | 15 / 0 | | V | | |
| | SKHI 22B on/off | | 5/0 | | V | | |
| V_{iT+} | Input threshold voltage (High) SKHI 22A | 10,9 | 11,7 | 12,5 | V | | |
| | SKHI 22B | 3,5 | 3,7 | 3,9 | V | | |
| V_{iT-} | Input threshold voltage (Low) SKHI 22A | 4,7 | 5,5 | 6,5 | V | | |
| | SKHI 22B | 1,5 | 1,75 | 2,0 | V | | |
| R _{in} | Input resistance SKHI 22A | | 10 | | kΩ | | |
| | SKHI 22B | | 3,3 | | kΩ | | |
| $V_{G(on)}$ | Turn on gate voltage output | | + 15 | | V | | |
| V _{G(off)} | Turn off gate voltage output | | - 7 | | V | | |
| R _{GE} | Internal gate-emitter resistance | | 22 | | kΩ | | |
| f _{ASIC} | Asic system switching frequency | | 8 | | MHz | | |
| t _{d(on)IO} | Input-output turn-on propagation time | 0,85 | 1 | 1,15 | μs | | |
| t _{d(off)IO} | Input-output turn-off propagation time | 0,85 | 1 | 1,15 | μs | | |
| t _{d(err)} | Error input-output propagation time | | 0,6 | | μs | | |
| t _{pERRRESET} | Error reset time | | 9 | | μs | | |
| t _{TD} | Top-Bot Interlock Dead Time SKHI 22A | 3,3 | | 4,3 | μs | | |
| | SKHI 22B | no interlock | | 4,3 | μs | | |
| V_{CEsat} | Reference voltage for V _{CE} -monitoring | | 5 ²⁾ | 10 | V | | |
| C _{ps} | Coupling capacitance primary secondary | | 12 | | pF | | |
| MTBF | Mean Time Between Failure T _a = 40°C | | 2,0 | | 10 ⁶ h | | |
| w | weight | | 45 | | g | | |

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